

New Power Electronics Magazine Bio Guidelines

The intent of a biography in the *IEEE Power Electronics Magazine* is to provide the reader with the author's relevant background and qualifications concerning the topic of the article or column. Consequently, the biography is not similar to a vitae, nor even to the biography that might be used with a journal article. Additionally, an author's biography may change from article to article to reflect appropriate relevance to the topic of the specific magazine article. The author provides a biography that meets this intent according to the guidelines below. Only feature articles and columns have biographies. Only columns have an author image in addition to the biography.

- There is a 150-word limit per author for 3 or fewer authors
- There is a 100-word limit per author if 4 or more authors
- There is no minimum word count, but the biography must include the name and email, as well as the current role and affiliation sufficient to convey relevance to the article topic.
- The biography should begin with the author's name, titles (if desired), and email address, followed by their current affiliation (paid or unpaid) and role.
- The remaining information contained in the biography can include education and degree information, past affiliations, awards, society and board positions, rewards and honorary memberships, authorships and editorial positions, patent information, market size information, application areas, as well as stating relevant career goals or interest areas.
 - Affiliation roles are recommended to be in reverse chronological order, but not required
- A high-resolution headshot photo must also be provided, in JPEG, JPG, or PNG format.

Examples

Katherine A. Kim (katherine.kim@ieee.org) is a Professor of Electrical Engineering at National Taiwan University (NTU), Taipei, Taiwan. Dr. Kim received the IEEE PELS Richard M. Bass Outstanding Young Power Electronics Engineer Award in 2019 and the IEEE PELS Award for Achievements in Power Electronics Education in 2022. She has served in various roles for IEEE PELS and is currently the Vice President for Educational Activities. Previously, she was an Assistant Professor of Electrical and Computer Engineering (ECE) at Ulsan National Institute of Science and Technology (UNIST), South Korea. She received the B.S. degree in ECE from Franklin W. Olin College of Engineering and the M.S. and Ph.D. degree in ECE from the University of Illinois at Urbana-Champaign. Her "KatKimShow" YouTube channel of engineering lecture videos has almost 100,000 subscribers and over 12 million views. Dr. Kim strives to make power electronics education more accessible and engaging for people worldwide.

Stephanie Watts Butler, Ph.D., P.E. (Senior Member, IEEE) (s.w.butler@ieee.org) is currently the President of WattsButler LLC, an innovation services company focused on the power semiconductor industry. During her previous career at Texas Instruments, she produced innovations in the areas of power and CMOS process and package technology, processing equipment, materials, reliability, research and development management, manufacturing science, control, fault detection, metrology, and new product development generating 17 U.S. patents. She is the Co-Founder and the Past-Chair of JEDEC's JC-70 Wide Bandgap Standards Committee and the Co-Convenor of IEC's TC47/WG8. She is the Industry Deputy Editor-in-Chief of IEEE Power Electronics Magazine, founding Chair of the PELS Industry Committee, and a WIE Committee Member. She serves on the APEC Planning Committee and the PSMA Board of Directors. She is a fellow of AVS and a recipient of SWE's Lifetime Achievement Award for Outstanding Technical Contributions.

Doug Bailey (doug.bailey@power.com) joined Power Integrations in 2004 as vice president of marketing. Before joining PI, He served as vice president of marketing at ChipX, a structured-ASIC company. His earlier experience includes serving as a business management and marketing consultant for Sapiential Prime, Inc., director of sales and business unit manager for 8x8, Inc., application engineering management for IIT, Inc., and design engineering roles with LSI Logic, Inmos, Ltd., and Marconi.